



GUJARAT POWER ENGINEERING AND RESEARCH INSTITUTE

TIME TABLE FOR THE FIRST YEAR OF ACADEMIC YEAR 2017-2018, EVEN SEM

W.E.F. 11TH January, 2018

Branch Code: 07	Semester: II	Division: B (Computer/Electrical Engg.)	Room No.: 004									
	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday						
9:00am to 9:55am	BE (HP)	BE (HP)	CPD (VR)	VCLA(C1) (HAP) (004)	VCLA(C2) (ARP) (006)	ES (HM)						
9:55am to 10:50am	CPD (VR)			EC & CC Activities								
10:50am to 11:45am	B21 BE (HP) 209-B	B22 EG (BYP) 007	B23 CPU (HP) 207-B Ex.				B21 CPU (TUT) (RPP) 115-A	B22 CPU (TUT) (HP) 115-B	B23 INTE RNET SLOT (013)(I G)			
11:45am to 12:30pm	RECESS											
12:30pm to 1:25pm	VCLA(C1) (HAP) (004)	VCLA(C2) (ARP) (006)	VCLA(C1) (HAP) (004)	VCLA(C2) (ARP) (006)	ES (HM)	CPD (AT)	B21 & B22 INTERNET SLOT (013) (IG)	B23 CPU (TUT) (HP) 115-B	EC & CC Activities			
1:25pm to 2:20pm	CPU (MP)		CS (AT)		VCLA(C1) (HAP) (004)	VCLA(C2) (ARP) (006)	ES (HM)	BE (HP)				
2:20pm to 2:35pm	RECESS											
2:35pm to 3:30pm	B21 EG (BYP) 007	B22 CPU (MP) 013 Ex.	B23 BE (HP) 209-B	EG (BYP)		CPU (MP)		CPU (MP)		VCLA (HAP) (004)	VCLA (ARP) (006)	EC & CC Activities
3:30pm to 4:25pm	LIBRARY SLOT			EG (PGP)		BE (HP)		CPD (AT)		EC & CC Activities		
VLCA	Vector Calculus & Linear Algebra (2110015)				HAP, ARP		Prof.Himesh Patel & Dr. Amisha Patel					
EG	Engineering Graphics (2110013)				BYP, PGP		Prof. Bhargav Pandya, Prof. Parin Patel					
CPU	Computer Programming & Utilization (2110003)				MP, HP, RPP		Prof. Madhuri Patel , Prof. Happy Patel , Prof. Rajesh P Patel					
BE	Basic Electronics (2110016)				HP		Prof.Hiten Panchal					
ES	Environment Science (2110007)				HM		Prof.Harsh Morebiya					
CPD	Contributor Personality Development (2990001)				VR, AT		Dr. Vikas Raval, Prof. Alkesh Trivedi					

Faculty In-charge

HOD

Principal