

Chirag Vibhakar

- **Designation** : Associate Professor
- **Experience** : 23 years Teaching and 1 years industrial
- **Educational Qualification** :APDF, Ph.D
- **Specialization** :High Voltage Engineering
- **Mail Id** :chirag.vibhakar@jgperi.ac.in

Research

Investigation on RTV silica coating in solid insulating at CPRI, Bangalore during Dec 2015 to Dec 2016

Partial discharge Analysis in Solid insulator material at ERDA, Vadodara.

1. C. K. Vibhakar and S. A. Kanitkar, "Investigation on Time domain Signal to Appropriate Wavelet Transform Threshold Function Selection for Partial Discharge Detection", IJEEI Paper Review ID no: IJEEI-L10-10181 Year 2011 Impact factor – 0.2815 page no from 74 to 82.

2. C. K. Vibhakar and S. A. Kanitkar, "A Novel approach for wavelet transform threshold function selection using acquired signal analysis for partial discharge extraction", Institute of Engineer, India

3. C. K. Vibhakar and S. A. Kanitkar, "A Novel approach for wavelet transform threshold function selection using acquired signal analysis for partial discharge extraction", IEEMA, Mumbai, India Year 2011 Page no From 138 to 142

4. C. K. Vibhakar and S. A. Kanitkar Presented a paper on "Novel investigation for specimens ageing test under PDs and temperature." in International Level Presentation Conference Organized by IEEE during 18-21 Oct. 2009 at CEIDP Virginia, USA.

5. C. K. Vibhakar and S. A. Kanitkar Presented a paper on "investigation on epoxies aging using Partial Discharge (PD) phenomena under different temperature and modeling of PD extraction using Wavelet analysis" in International Level Presentation Conference Organized by IEEE, DEIS. During Oct 11th-14th, 2010 at ICHVE, New Orleans, USA.

6. C. K. Vibhakar and S. A. Kanitkar Presented a paper on "Investigation and analysis of insulators aging tests under the effect of temperature on Partial Discharge" in International Level Presentation Conference Organized by IEEE during 5-8 Dec. 2010 at AUPEC, Auckland, Newzealand.

7. C. K. Vibhakar and S. A. Kanitkar Presented paper on "Ageing in solid insulating material by studying the effect of variation of parameters", during 13-14 Oct 2005 by National Conference on Engineering Science, C. U. Shah, Wadhwan, India.

8. C. K. Vibhakar and S. A. Kanitkar Presented Paper on "A Study Survey on Solid Insulating Material by Short Term Ageing Test", Oct 2005, By National conference on emerging Technologies and applications by Amoghsiddhi education Society, Sangly-416415

9. Presented a paper on "Reduction of T & D Losses" in National Level Presentation Conference Organized by S.V.I.C., during 8-9 Jan 2005, Kadi & A.E.S. Sangli

10. Presented paper on "Transmission and Distribution losses in Indian Power System-an analysis and step to reduce" conference organized by National Power Engineering Conf.(NPEC), during 16-17 Dec. 2005, TCE, Madurai, Tamilnadu

11. C. K. Vibhakar and S. A. Kanitkar Presented a paper on "Investigation and modeling of epoxies ageing test under the effect of variation of parameter on Partial Discharge (PD)" in

International Level Presentation Conference Organized by IEEE during 17-24 Dec. at ICPS IIT Khadakpur, India

12. C. K. Vibhakar and S. A Kanitkar Presented Paper on “A new deterministic model for tree growth in insulating materials with barriers”, during 1-2 Oct2006, By National conference on emerging Technologies and applications by computer science department Saurashtra university, Rajkot, India

13. C. K. Vibhakar and S.A Kanitkar Presented a paper on “Modeling and Investigation of PD in Artificial Voids in Solid Insulating Material Used as a Capacitor” in international Level Presentation Conference Organized by Saurashtra University and getting 2nd rank at ICETA During 13-14, Jan2008,Rajkot, India

14. C. K. Vibhakar and S. A. Kanitkar Presented a paper on “Novel investigation on partial discharge signal in power transformer using wavelet transform” in International Level Presentation Conference (forum) Organized by 8th India Doble power forum program. During Dec.2nd - 4th, 2010 at PD Monitoring of High Voltage Assets at Modern Trends, Ahmedabad,India (Recognized by USA Doble Engg. co).

15. C.K. Vibhakar “Voltage Sag/ Swell Compensator Using Dynamic Voltage Restorer (DVR)”, International Journal of Application of Innovation in Engineering & Management. (IJAIEEM)- 2014, Impact Factor 2.37 Page No: 53-61.

16. C.K. Vibhakar “Simulation of Shunt Active Filter for Compensation of Non-linear Load Currents”, International Journal of Engineering Development & research.(IJEDR)- 2014, Impact Factor 1.79 Page No:1588-1594.

17. C.K. Vibhakar “Investigation of International Standard, International Electro Technical Commission, IEC 62035 and Indian Standard IS2309 in positioning the Air Terminals”, International Journal of Engineering Development & Research.(IJEDR)- 2014, Impact Factor 1.79 Page No:1595-1597.

18. C.K. Vibhakar “Over view of Different wind power technology connected to grid and modeling of wind turbine.” International Journal of Engineering Development and Research (IJEDR), Year 2013 Impact Factor 1.76 Page No: 53-61.

19. C. K. Vibhakar and S. A. Kanitkar, “Extraction of PD Signals using Wavelet Transform with Different Thresholding Methods”, International Journal of Electrical & Electronics Engineering. Accepted Code SERC-IJEEE-SU10-04-25L. , ISSN 0974-2042, Vol.-12, Issue no.- 03, Jun.2010-Aug-2010

20. C. K. Vibhakar published “Voltage Profile Improvement Using TCSC for IEEE-6 Bus System Using Trial And Error Method” on International Journal of Engineering Research and Development (IJERD) .impact factor 1.79, Page No: 37-40 April 2015

21. C. K. Vibhakar published “Control Strategy of Induction Generator Connected to Grid.” International Journal of Engineering Research and Development (IJERD) on April 2015, page no: 6-13 with impact factor 1.79.

22. C. K. Vibhakar published "Power quality Improvement BLDC Motor using BUCK Converter..." International Journal of Engineering Research and Development (IJERD) on April 2015, page no: 15-20 with impact factor 1.79.

23. C. K. Vibhakar published "Control Strategy of Induction Generator Connected to Grid." International Journal of Engineering Research and Development (IJERD) on April 2015, page no: 6-13 with impact factor 1.79.

24. C. K. Vibhakar published "Harmonic mitigation for power quality improvement by designing phase shifting transformer for nonlinear loads." International Journal of Engineering Research and Development (IJERD) on MAY 2015, page no: 1418-1425 with impact factor 1.79.

25 C. K. Vibhakar published "A novel approach wavelet transform threshold function selection" IEEMA vol-3 no.1-Sep. 138-142 Mumbai, India 2011.

26 C. K. Vibhakar published on "Time domain signal to appropriate wavelet transform threshold function selection" International Journal on Electrical Engineering and Informatics Investigation, vol-3 nov-1 74-82 ISSN 2085-6830 Indonesia 2011.

27 C. K. Vibhakar published on "Pulling out partial discharge signals using wavelet transform with different thresholding methods" in The Indian Journal of Technical Education, vol-33 no3, July 40-44 ISTE New Delhi 2010.

Professional Contribution

1. Delivered “Expert Lecture” on the STTP of Recent Trends in Power system on the topic High Voltage Analysis at Charotar University of Science and Technology, Gujarat.
2. Delivered “Expert Lecture” on the STTP of Recent Trends in Power system on the year 2014 in topic Partial Discharge Analysis on High Voltage at R. K. University, Rajkot, Gujarat
3. Delivered “Expert Lecture” on the STTP of Power System and Mat lab Applications conducted on September 2014 in the topic Partial Discharge Analysis on High Voltage at V.V.P Engineering College, Rajkot.

TRAINING PROGRAMMES/WORKSHOPS/SCHOOLS ATTENDED

- 1) Attended One Day Workshop on “Microcontroller and PC Applications To Instrumentation and Control” organized by Electrical Engineering Department, BVM, Vallabh Vidyanagar, on April 25, 2001
- 2) Attended a four day workshop on “Electrical System Design Using Electronic Work Bench”, conducted by Department of Electronics and Communication, DDIT Nadiad, during February 5-8, 2002.
- 3) Participated in State Level on “Geographical Information System, Global Positioning System and Remote Sensing”, organized by Electrical Engineering Department and Electronics and Communication Department, V.V.P. Engineering College, Rajkot-360005 sponsored by GUJCOST and RESECO, on September 14, 2003.
- 4) Participated in IIP Cell Seminar on “An Integrated Approach For Communication Skill Development”, Organized at Institute of Technology, Nirma University, Ahmedabad, on December 15, 2003.
- 5) Participated in National Seminar on “Latest Trends in Communication Systems and Technology”, organized by Department of Electronics and Communication, A.D. Patel Institute of Technology, New Vallabh Vidyanagar, Gujarat on March 13, 2004.

- 6) Participated in one day National Level Seminar on “Efficient Utilization of Electrical Energy” organized by Electrical Engineering Department, AITS, Rajkot, sponsored by AICTE, on October 24, 2004.

- 7) Participated in a National Level Workshop on “Matlab for Process Engineers”, organized by Chemical Engineering Department, V.V.P. Engineering College, Rajkot-360005, sponsored by AICTE, from July 16-18, 2004.

- 8) Attended the QIP Short Term Course on “Intelligent Sensors and Controls”
Organized by Department of Electrical Engineering, IIT Kanpur, during May 04-10, 2005.

- 9) Participated in National Level Seminar on “Bioinformatics-Emerging Trends”, organized by Shri Manibhai Virani and Smt. Navalben Virani Science College, Rajkot, sponsored by UGC on January 1st, 2006.

- 10) Attended seminar on “Environment Traffic Pollution Problems and Remedies”, organized by V.V.P. Engineering College ISTE Chapter, Rajkot-360005, on April 9th, 2006

- 11) A three day workshop on “Testing in Electrical Engineering” arranged under Institutional Networking Scheme of TEQIP Project organized by Department of Electrical Engineering, Sardar Vallabhbhai National Institute of Technology from 3-5th January, 2007.

- 12) Attended one day State Level “Energy Conservation Awareness Workshop” organized by Gujarat Energy Development Agency (GEDA) at AITS, Rajkot, March 1, 2007.

- 13) Short term course (one week) on “Recent Advances in Testing and Design of Electrical Insulation” conducted by Department of Electrical Engineering, IIT Kanpur, from December 17-23, 2007

- 14) Attended a short term training program on “Wind Energy” organized by Civil, Applied Mechanics and Mechanical Engineering Department, Sardar Vallabhbhai National Institute of Technology, sponsored by AICTE, from January 19-23, 2009.

- 15) One week training on “Modern Trends In Demand Side Management (DSM) and Non-Conventional Energy Sources (NCES)”, from May 11-15, 2009, sponsored by GUJCOST,

Gandhinagar and conducted by Department of Electrical Engineering, Lukhdhirji Engineering College, Morbi.

16) Expert Lecture at STTP held at Electrical Engineering. Dept. at V.V.P Engineering College E.C Department, July 2013

17) Organized STTP in "Recent Trend in Power system & Matlab application" on July 1-5 / 2013, as a coordinator

18) Organized ISTE National Level event ON 21ST SEP 2013 AT V.V.P Engineering College, Rajkot

19) Expert Lecture at STTP held at Electrical Engineering. Dept. at RK University, Rajkot 18th November 2013

20) Attended National Conference on recent advances in Technology and Management for Integrated growth 2013(RATMIG 2013) on title "Improved power quality of BLDC motor -using buck DC-DC Converter" , held on 9th and 10th December 2013.

21) Attended National Conference on recent advances in Technology and Management for Integrated growth 2013(RATMIG 2013) on title "Voltage Sag/Swell Compensation Using Dynamic Voltage Restorer", held on 9th and 10th December 2013.

22) Attended National Conference on Recent Trends in Electrical and Electronics for Communication Engineering (RTEECE 2014), HELD ON 17th and 18th January 2014, at ITM Universe, Baroda

23) Organized and Delivered Expert Lecture in STTP held at Electrical Engineering. Dept. at VVP Engineering College, Rajkot on September 2014

24) Member of organizing committee of ISTE National Event at VVP Engineering College, On September 2014

25) Organized a 1 day workshop of GEDA at VVP Engineering College on 19th March 2015

Achievements

➤ BOOKS PUBLISHED

High Voltage Engineering- 2019, ISBN: 978-93-5192-221-6

- Awarded at 4th GTU Innovation Sankul Day Celebration Pedagogical Innovation Awards for appreciation towards my role as

[1] Academia Co-Chair and

[2] Continuing efforts at Paryavaran Mandir-Ahmedabad on 14th Feb. 2015

- Life Member- Indian Society for Technical Education [LM 37374]

- Fellow - IEI [M 14557-3]